

**In the Specification:**

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[0021] Referring now to FIG. 3, there is a schematic diagram of a phase correction circuit of the present invention that may be used with a remote mobile receiver. This phase correction circuit receives signals R.sub.j.sup.1 and R.sub.j.sup.2 as input signals on leads 610 and 614 as shown in equations [5-6], respectively.

$$R_j^1 = \sum_{i=0}^{N-1} r_j(i + \tau_j) = \alpha_j^1 S_1 - \alpha_j^2 S_2^* \quad [ 5 ]$$

$$[[ R_j^2 = \sum_{i=N}^{2N-1} r_j(i + \tau_j) = \alpha_j^1 S_{12} + \alpha_j^2 S_1^* ]]$$

$$\underline{R_j^2 = \sum_{i=N}^{2N-1} r_j(i + \tau_j) = \alpha_j^1 S_2 + \alpha_j^2 S_1^*} \quad [ 6 ]$$